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A Novel Arc Suppression Coil

Abstract. The arc suppression coil (ASC) is one of the most important devices in distribution networks to obtain the optimal power supply quality. Though there exits many types of ASC, some problems have been still not solved. In order to develop an ASC with rapid and continuous tuning and limited harmonics, this paper proposes a novel type of ASC, which is based on a single phase matrix converter. Due to pulse width modulation (PWM) control, compensating currents can be adjusted in a wide range and the order of harmonics is so high to be filtered easily. Finally, by using PSCAD/EMTDC, simulations are given to illustrate the feasibility and validity of this novel ASC.

Streszczenie. W artykule zaproponowano nowy typ dławika gaszącego ASC, bazujący na jednofazowym przekształtniku matrycowym. Dzięki modulacji PWM, możliwe jest generowanie prądów kompensujących o szerokim zakresie, a co za tym idzie skuteczna eliminacja harmonicznych. Wykonano badania symulacyjne w programie PSCAD/EMTDC, weryfikujące skuteczność działania rozwiązania. (**Dławik gaszący ASC).**

Keywords: Arc suppression coil, Resonant grounding, Compensated distribution network, Single-line-to-ground fault. Słowa kluczowe: dławik gaszący, uziemienie rezonujące, sieć skompensowana, zwarcie doziemne jednofazowe.

Introduction

An arc suppression coils (ASC) is an adjustable reactor connected between the neutral point and the ground, which was invented by Petersen in 1916 to suppress the falt current between the fault point and the ground (consequently it is also called Petersen coil). When a single-line-to-ground (SLG) fault occurs, by properly tuning the ASC, a lagging reactive current from the ASC and a leading current from the line-to-ground capacitance can be made almost exactly equal, so that the fault current is substantially zero. Under this condition the arc can not maintain itself, and is made self-extinguishing [1-3].

It is beneficial that the circuit will remain connected without tripping circuit breakers in the event of a SLG fault. This solution offers many advantages such as significant and economical improvements in safety and power supply quality. Therefore, the ASC is widely used in the 6-35kV distribution networks in China.

Until now many kinds of ASCs have been applied in the MV distribution network. For example, plunger core coils require a precise mechanical drive system, have a long response time and many noise [3-5]. Winding turned ASCs response more quickly and produce few harmonics, but can not continuously adjust the impedance [3, 6, 7]. Capacitor tuning ASCs use capacitors to cancel the inductive current generated from the reactor, but due to aging the capacitor value will become smaller as time goes on [8]. Magnetic controlled ASCs are more expensive [9-11]. Thyristor controlled ASCs overcome above drawbacks, but it is known that this kind of ASC generates low order harmonics. The unwanted low order harmonics are difficult to reduce [3, 12, 13].

In recent years some research scholars start to utilize the power electronic technology to design ASCs. [14] and [15] introduce an ASC based on transformer with controlled inverter load. The compensating current is generated by a DC/AC inverter. This voltage source pulse width modulation (PWM) inverter works as the secondary load of transformer, the reactance of the ASC can be regulated arbitrarily through controlling the inverter output current. However, one or more DC power is needed.

To overcome these drawbacks, this paper proposes a novel ASC, which is based on the single-phase matrix converter (SPMC) principle and the PWM technology.

The circuit topology and principles

As shown in Fig.1, the novel ASC consists of a single phase matrix converter (SPMC), a reactor, a 3-winding step-down transformer and a low-pass filter. If a neutral point exits, the ASC can directly connect to it. If there is no wye point available for ASC connection to ground, an artificial neutral must be created on delta connection systems.



Fig.1. Simplified equivalent circuit diagram of a distribution network in the event of a SLG fault

The SPMC, which was first realized by Zuckerberger [16-18], is a forced commutated converter that uses an array of controlled bi-directional switches as the main power elements to create a variable output voltage system with unrestricted frequency. It does not have any dc-link circuit and does not need any large energy storage elements.

The SPMC consists of four bi-directional switches capable of blocking voltage and conducting current in both directions as show in Fig. 1 [16]. Because the bi-directional switch is not available to date, it can be implemented by connection of two diodes and two IGBTs in anti-parallel (common emitter back to back) as shown in Fig. 1. The IGBT were used because of its high switching capabilities and high current carrying capacities desirable for high-power applications.

Owing to limitations of low rated voltage of IGBT, a 3winding step-down transformer is needed. The low-pass filter, which is connected to the tertiary winding, is used to filter high order harmonics in compensating currents. For the sake of clarity, the 3-winding transformer and the lowpass filter are negligible in the following discussion.

Gating signals are generated by comparing a reference signal u_r with a triangular carrier wave u_c of frequency f_c as shown in Fig. 2, where the symbols U_{cm} , U_{rm} are the peak amplitude of the carrier and the reference signal, respectively.



Fig.2. Gating signals of the bi-directional switches

If the output current i_o is greater than zero, the switch S_{Ib} and S_{4b} turn on. The output current i_o passes through the neutral point N, the switch S_{1b} , R, L and another switch S_{4b} . The switch S_{4b} is controlled by PWM signals and in this positive half cycle, when S_{4b} is turned off, S_{2a} will be turned on and take the place of S_{4b} to provide a freewheeling path to discharge the stored energy.

If the output current i_o is lower than zero, the switch S_{la} and S_{4a} turn on. The current i_o passes through the neutral point N, the switch S_{1a} , R, L and another switch S_{4a} . The switch S_{Ia} is controlled by PWM signals and in this negative half cycle, when S_{1a} is turned off, the switch S_{3b} will be turn on and take the place of S_{4a} to provide a freewheeling path.

It's worth noting that gating signals of S_{2a} , S_{3b} are complement to that of S_{4b} , S_{1a} , respectively. In Fig. 2, G_{ia} and G_{ib} (*i*=1, 2, 3, 4) are the gating signals of S_{ia} and S_{ib} (*i*=1, 2, 3, 4), respectively.

Analysis of the working current

When a SLG fault occurs, the neutral voltage will increase, the size of which depends on the fault resistance. For simplicity, let the neutral voltage be

(1)
$$u_N = U_m \sin \omega t$$

where: U_m is the peak neutral voltage, $\omega = 2\pi f$ and f is the frequency of the network. In the following analyses, switch devices as well as the diodes are considered as ideal elements. In Fig. 2, the triangular carrier u_c can be expressed as shown in (2).

(2)
$$u_{c} = \begin{cases} -\left[\omega_{c}t - (2k-1)\pi\right]\frac{U_{cm}}{\pi}, \\ \omega_{c}t \in \left[2(k-1)\pi, (2k-1)\pi\right] \\ \left[\omega_{c}t - (2k-1)\pi\right]\frac{U_{cm}}{\pi}, \\ \omega_{c}t \in \left[(2k-1)\pi, 2k\pi\right] \end{cases}$$

where: $\omega_c = 2\pi f_c$, $k=1, 2, 3, \cdots$

Fig. 3 shows the k triangular carrier u_c crosses the reference signal u_r at points of α_k and β_k , which can be represented as shown in (3).

(3)
$$\begin{cases} \alpha_k = (2k-1)\pi - M\pi \\ \beta_k = (2k-1)\pi + M\pi \end{cases}$$

where: $k=1, 2, 3, \dots$ and M is define as the modulation factor, $M = U_{rm}/U_{cm}$. Obviously, $0 \le M \le 1$.



Fig.3. The triangular carrier u_c and the reference signal u_r

If $\alpha_k \le \omega t \le \beta_k$ ($k=1, 2, \dots, N/2$), the circuit is at ON state. The working current i_s flows into the ground through R and L. According to Kirchhoff's Voltage Law, the output current i_o can be solved from (4).

(4)
$$u_N = i_o R + L \frac{di_o}{dt}$$

With the initial condition described in (5), the solution of (4) is given as (6).

(5)
$$i_o(\omega t)\Big|_{\omega t = \alpha_k} = I_o(\alpha_k)$$

 $i_o = \frac{U_m}{|Z|} \sin(\omega t - \varphi) +$
(6) $-\frac{R_c(\omega t - \alpha_k)}{|Z|} \int_{Z_c(\omega t - \alpha_k)} U$

$$e^{-\frac{\pi}{\omega L}(\omega t - \alpha_k)} \left[I_o(\alpha_k) - \frac{C_m}{|Z|} \sin(\alpha_k - \varphi) \right]$$

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where the reactor impedance $Z=R+j\omega L$ and the angle φ =arctan ($\omega L/R$).

At the end of the ON state in the steady state condition. $i_o(\beta_k) = I_o(\beta_k)$. With this condition applied to (6), the output current $I_o(\beta_k)$ is

(7)
$$I_{o}\left(\beta_{k}\right) = \frac{U_{m}}{|Z|} \sin\left(\beta_{k} - \phi\right) + e^{-\frac{R}{\omega L}\left(\beta_{k} - \alpha_{k}\right)} \times \left[I_{o}\left(\alpha_{k}\right) - \frac{U_{m}}{|Z|} \sin\left(\alpha_{k} - \phi\right)\right]$$

During the ON state, the working current i_s equals i_o as shown in (8).

(8) $i_s = i_o$

If $2(k-1)\pi \le \omega t \le \alpha_k$ or $\beta_k \le \omega t \le 2k\pi$ ($k=1, 2, \dots, N/2$), the circuit is at OFF state. Similarly, with Kirchhoff's Voltage Law applied, the output current i_o can be solved from (9).

(9)
$$0 = i_o R + L \frac{di_o}{dt}$$

The initial value of i_o at $\omega t = \beta_k$ is given in (7). Then the solution of (9) is got as (10).

(10)
$$i_o = I_o(\beta_k) e^{-\frac{R}{\omega L}(\omega t - \beta_k)}$$

At the end of the OFF state, $\omega t = \alpha_{k+l}$. The output current i_o is given as (11).

(11)
$$I_{o}(\alpha_{k+1}) \triangleq i_{o}(\omega t)|_{\omega t = \alpha_{k+1}}$$
$$= I_{o}(\beta_{k}) e^{-\frac{R}{\omega L}(\alpha_{k+1} - \beta_{k})}$$

And this value is the initial value of the next ON state. During the OFF state, the working current i_s is zero as shown in (12).

(12) $i_s = 0$

The two modes as described above are regularly repeated and the output current i_o is continuous, but the working current i_s is discontinuous. The waveforms of the output current i_o and the working current i_s during a switching period $[2(k-1)\pi, 2k\pi]$ are respectively given from (13) and (14).

$$(13) \qquad i_{o} = \begin{cases} \frac{U_{m}}{|Z|} \sin(\omega t - \phi) + e^{-\frac{R}{\omega L}(\omega t - \alpha_{k})} \Big[I_{o}(\alpha_{k}) - \frac{U_{m}}{|Z|} \sin(\alpha_{k} - \phi) \Big], \alpha_{k} \le \omega t \le \beta_{k} \\ I_{o}(\beta_{k}) e^{-\frac{R}{\omega L}(\omega t - \beta_{k})}, \quad \text{others} \end{cases}$$

$$(14) \qquad i_{s} = \begin{cases} \frac{U_{m}}{|Z|} \sin(\omega t - \phi) + e^{-\frac{R}{\omega L}(\omega t - \alpha_{k})} \Big[I_{o}(\alpha_{k}) - \frac{U_{m}}{|Z|} \sin(\alpha_{k} - \phi) \Big], \alpha_{k} \le \omega t \le \beta_{k} \\ 0, \quad \text{others} \end{cases}$$

Harmonics analyses of the working current

Let an input $f_{in}(t) = F_m \sin \omega t$ be controlled by the PWM signals as shown in Fig. 2, the output $f_o(t)$ can be expressed as (15).

(15)
$$f_o(t) = \begin{cases} f_{in}(t), & \alpha_k \le \omega t \le \beta_k \\ 0, & \text{others} \end{cases}$$

According to the double Fourier series [19], the output $f_o(t)$ can be described in a Fourier series as shown in (16) or (17).

(16)
$$f_{o}(\omega t) = MF_{m} \sin \omega t + \sum_{n=1}^{\infty} (-1)^{n} \frac{F_{m} \sin nM\pi}{n\pi} \sin (\omega t \pm n\omega_{c} t)$$

(17)
$$f_o(\omega t) = F_m \sin \omega t \cdot g(\omega_c t)$$

where:

(18)

$$g(\omega_c t) = M + \sum_{n=1}^{\infty} (-1)^n \frac{2\sin nM\pi}{n\pi} \cos n\omega_c t.$$

Thus, the output voltage u_o can be put in form of (19). $u_o = MU_m \sin \omega t + \omega$

(19)
$$\sum_{n=1}^{\infty} (-1)^n \frac{U_m \sin nM\pi}{n\pi} \sin \left(\omega t \pm n\omega_c t\right)$$

Obviously, the fundamental component of the output voltage u_{ol} is

 $(20) u_{o1} = MU_m \sin \omega t$

The output current i_o can be yielded as (21).

(21)

$$i_o = \frac{MU_m \sin(\omega t - \chi_1)}{|Z_1|} + \frac{\sum_{n=1}^{\infty} (-1)^n \frac{U_m \sin nM\pi}{n\pi} \sin(\omega t \pm n\omega_c t - \chi_n)}{|Z_n|}$$

where: $Z_n=R+j\omega L$ and $\chi_n=\arctan(n\omega L/R)$ are the *n* harmonic impedance and its phase angle. For a special example, if $n=1, \chi_1=\arctan(\omega L/R)=\varphi$.

Similarly, the working current i_s can be derived from $i_s = i_o g(\omega_c t)$, then (22)

$$i_{s} = \left[M + \sum_{n=1}^{\infty} (-1)^{n} \frac{2 \sin nM \pi}{n\pi} \cos n\omega_{c} t \right] \times \\ \left[\frac{MU_{m} \sin (\omega t - \chi_{1})}{|Z_{1}|} + \frac{\sum_{n=1}^{\infty} (-1)^{n} \frac{U_{m} \sin nM \pi}{n\pi} \sin (\omega t \pm n\omega_{c} t - \chi_{n})}{|Z_{n}|} \right]$$

Thus, the fundamental component i_{s1} is

(23)
$$i_{s1} = \frac{M^2 U_m \sin(\omega t - \chi_1)}{|Z_1|}$$

It is obviously seen from (21) and (22) that the fundamental component of the output current i_o is linearly proportional to the modulation factor M, but that of the working current i_s is not. The value of fundamental working current i_s can be controlled in a wide range by adjusting the modulation factor M, which can be simply obtained by comparing a triangular wave with a reference signal. However, the working current i_s is not continual. When it is zero, the neutral point will become floating. Additionally, the ASC can only compensate the fundamental component of the fault current. So we can utilize filters to reduce harmonics and obtain the continual fundamental component.

The dominant harmonics of the current are near the switching frequency and its multiples $(\omega \pm n\omega_c)$. Compared with thyristor controlled ASCs, due to the high switching frequency, these high order harmonics contents are more easily filtered. Therefore, only a low-pass filter can reduce the harmonics in the working current and maintain the continuity of compensating currents. This is one of advantages.

Simulations

This section consists of two parts: one is to verify the relationship between the working current of the ASC and the modulation factor M of the SPMC; the other is to validate the compensated effect of the ASC in a distribution network. The well-known PSCAD/EMTDC program was used to analyze how this ASC work [23].

Part 1: a voltage source with 5.774kV takes the place of the neutral voltage (In fact, the neutral voltage is nearly 5.774kV when a SLG fault occurs in a 10kV distribution network). Parameters in this simulation are listed in Table 1. By changing the modulation factor *M*, we can get different working currents as shown in Table 2, where the second column lists the fundamental component of working currents without the low-pass filter, while results shown in

the third column describes the effect of the low-pass filter. Obviously, due to the low-pass filter, the working current becomes smaller.

Table 1. The simulation parameters in Part 1

Reactor	<i>L</i> =7.352 mH	
Step-down transformer	6/1.2/0.5 kV	
Low-pass filter	<i>C_f</i> =144 μF, <i>L_f</i> =0.085 mH	

Table 2. Working currents

Modulation factor	No low-pass filter [A]	Low-pass filter [A]
0.1	1.1	0.6
0.2	4.0	2.6
0.3	9.0	7.1
0.4	16.1	13.4
0.5	25.1	20.9
0.6	36.2	29.4
0.7	49.2	38.6
0.8	64.1	48.1
0.9	81.0	57.8

Fig. 4 shows a 2-cycle wave of the working current in case of no low-pass filter and the modulation factor M=0.7. It is evident that the working current is not continuous and contains many harmonics. As result of a low-pass filter, the working current becomes smooth (Fig. 5).



Fig.4 The working current (no low-pass filter)



Fig. 5 The working current (with a low-pass filter)

Part 2: according to Fig. 1, a simple radial distribution network is constructed. This distribution network consists of 4 feeders. The power supply voltage is 10kV neglected the source impedance, which simulates an infinite power system. The length of each feeder is 2km, 6km, 12km and 13km respectively, whose parameters are as follows [24]: the positive sequence resistance R_1 =0.075 Ω /km, the positive sequence capacitance C_1 =0.318µF/km; the zero sequence resistance R_0 =0.102 Ω /km, the zero sequence inductance L_0 =0.892mH/km and the zero sequence capacitance C_0 =0.210µF/km.

A typical SLG fault was examined at the middle of the 13-km-long feeder. This simulation network has a total length of 33km, with the total capacitance current of 37.8A. According to Table 2, we should set the modulation factor M=0.7 so as to make the ASC work at a full-compensation state.

Fig. 6 and Fig. 7 show simulation results. When the SLG fault occurred at 0.103s, a transient over-current happened as shown in Fig. 6. Because of compensation of this novel ASC, the current-to-ground at the fault location drops from the maximum peak (165.1A) to 5.736A, to 3.239A, which are a small residual value (RMS<5A) as shown in Fig. 6. Therefore, the fault current can be limited to lower than 5A, so that the arc can not reignite. Due the fault resistance equals nearly zero, the neutral voltage increased to the phase voltage as shown in Fig. 7. When the fault disappeared at 0.410s, the neutral voltage decreases relatively slowly and it was not 866V (15% of the phase voltage 5.774kV) until t=1.145s (Fig. 7). That is to say, the recovery voltage in the fault phase is likewise slow. This characteristic is very beneficial for the suppression of the arc reignition.



Fig. 6 The fault current and its own RMS (the fault occurred at 0.103s and lasted 0.307s)



Fig. 7 The neutral voltage and its own RMS (the fault occurred at 0.103s and lasted 0.307s)

Conclusions

This paper presents a novel ASC, which is based on SPMC and PWM technology. Compared with other ASC such as winding turned ASC and the thyristor controlled ASC, this ASC not only can be continually tuned, but also generates much high order harmonic contents, which are easy to filter. If the value of the ASC is correctly tuned the current of a possible earth fault can be reduced to a suitable range. Simulations illustrate feasibility and validity.

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