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## SP8T Switch Design using Cascaded SPDT Switches for Monostatic Microwave Imaging System in Medical Application

**Abstract**. This paper proposes a single pole eight throw switch (SP8T) using cascaded single pole double throw (SPDT) switches. The switch design is to control eight antenna array in the monostatic microwave imaging system for medical application. A mathematical modeling was done for a simple series-shunt SPDT switch for the analysis of insertion loss, return loss and isolation. Then, a proper selection of SPDT switch topologies was carried out in a simulation software in order to get ultra wideband (UWB) performance of the SP8T switch design. As a result, the proposed SP8T switch produced 9.3 GHz of isolation bandwidth that can be used for a Gaussian pulse up to 9.3 GHz bandwidth (from 1.7 GHz to 11 GHz frequency spectrum).

Streszczenie. W niniejszym artykule zaproponowano jednobiegunowy przełącznik ośmiorurowy (SP8T) wykorzystujący kaskadowe jednobiegunowe przełączniki dwustanowe (SPDT). Konstrukcja przełącznika służy do sterowania układem ośmiu anten w monostatycznym mikrofalowym systemie obrazowania do zastosowań medycznych. Przeprowadzono modelowanie matematyczne prostego przełącznika SPDT z bocznikiem szeregowym do analizy tłumienia wtrąceniowego, tłumienia odbiciowego i izolacji. Następnie dokonano właściwego wyboru topologii przełącznika SPDT w oprogramowaniu symulacyjnym, aby uzyskać wydajność ultraszerokopasmową (UWB) projektu przełącznika SP8T. W rezultacie proponowany przełącznik SP8T wytworzył pasmo izolacji 9,3 GHz, które można wykorzystać dla impulsu Gaussa o szerokości do 9,3 GHz (widmo częstotliwości od 1,7 GHz do 11 GHz). (Konstrukcja przełącznika SP8T z wykorzystaniem kaskadowych przełączników SPDT do monostatycznego systemu obrazowania mikrofalowego w zastosowaniach medycznych)

**Keywords:** PIN diode switch, SP8T, SPDT, ultra wideband, UWB, microwave imaging. **Słowa kluczowe:** obrazowanie mikrofalowe, przełączniki sp8T, WWB<mark>.</mark>

### Introduction

In designing a microwave imaging system for medical application, it can use a monostatic radar approach by transmitting a Gaussian pulse (with an ultra wideband (UWB) frequency) through antennas [1, 2]. As depicted in Fig. 1, any microwave system using one antenna acts as both transmitter and receiver and the signal is analyzed by interrogating the reflecting signal [3, 4]. The antenna is an ultra wideband performance as reported in [5, 6] and a confocal algorithm is used to shape an image based on the captured reflected signal.



Fig. 1. Concept of monostatic radar in a microwave system [3]

Antenna array is normally used with half wavelength element-to-element spacing to estimate the real-time imagery of targets [7]. A minimum of eight antennas can be used in microwave imaging systems [8, 9, 10].

It is found that, Radio Frequency (RF) switches are commonly used for microwave imaging to support and control multiple antennas for transmit and receive signals. As depicted in Fig. 2, it is an example of RF switch used in breast imaging system for antenna array switching [11]. The RF switch must be an UWB performance for the insertion loss, return loss and also isolation.

However, due the unsuitable of RF switch design (e.g. wrong selection of switch topology), there will be RF leakages to another unused antenna, thus deteriorate the microwave imaging system performance. Hence, for the best RF switch design, it should be able to reduce a

leakage signal from off-path port during transmit or receive mode [12, 13]. Therefore, a proper design of RF switch is important to ensure the power of signals from the pulse generator is reaching to the antenna with minimum losses [14, 9] and a good isolation performance is needed to remove any leakage signal due to multiple antenna configurations in microwave imaging systems [15, 16].



Fig. 2. RF switch in breast imaging system for antenna array switching [11]

Therefore, this paper proposes a single pole eight throw (SP8T) switch for monostatic microwave imaging system in medical application. It was designed in the frequency range from 1 to 11 GHz. The switch design is to control eight antenna array in the system. The proposed SP8T switch is based on a cascaded single pole double throw (SPDT). With the proper selection of switch topology and switching element (e.g. PIN diode), UWB isolation performance of the RF switch can be achieved but need to look at the other trade off as well such as return loss or insertion loss performances.

### SP8T Switch Design

The proposed SP8T switch is shown in Fig. 3. In this figure, Port 1 is an input for transmit and receive of

Gaussian pulse signal, while Port 2 until 9 are connected to 8 antenna arrays. A cascaded of seven SPDT switches is the key element in the SP8T switch, due its capability to get high isolation performance by selecting a suitable SPDT switch topology.

Besides that, MACOM'S PIN diodes (MA4SPS402) were used in the SP8T switch as the switching elements. The PIN diodes were made from silicon glass. By referring to the product datasheet, these PIN diodes are suitable for high frequency applications which have a maximum frequency of 18 GHz. Hence, the selection of the PIN diode contributed also to the performance of the SP8T switch design.

The performance analysis of SP8T switch is based on the insertion loss, return loss and isolation. Take an example operation during transmission of Gaussian pulse signal that inserted into Port 1 and transmitted to antenna No. 2 at Port 2. The insertion loss is simulated or measured between Port 1 and Port 2 (S<sub>21</sub>) meanwhile the return loss is at Port 1 (S<sub>11</sub>). For the isolation, it can be simulated at any unused antennas (No 3 until No 9). In this paper the isolation was simulated and measured at antenna No. 3, between Port 1 and 3 (S<sub>31</sub>).



Fig. 3. Cascaded SPDT switches in UWB SP8T switch

### Selection of SPDT Switch Topology

The performance analysis of SPDT circuit is important to find the best topologies to be further developed in SPDT for the SP8T switch circuit. As reported in [17], designing a wideband and broadband RF switches are more challenging compared to narrowband RF switches. It requires very wideband or broadband of isolation, insertion loss and return loss. Broadband or wideband isolation can be achieved by combining several techniques such as RF switch topology and combined with the use of fabrication process of PIN diode or transistor. In our previous research work of RF switch for wireless communication [18], the series-shunt-shunt is the best topology for broadband isolation. Furthermore, the series-shunt PIN diode is the most chosen topology in order to get an ultra wideband isolation performance [19, 20, 21, 22, 23].

A mathematical modeling can be done for a simple series-shunt SPDT switch (Design A) as shown in Fig. 4. A basic series-shunt is selected for the mathematical analysis of the insertion loss, return loss and isolation. Considering a transmission of the Gaussian pulse from Port 1 to Port 2. Then, the insertion loss can be analyzed as S<sub>21</sub>, the return loss as S<sub>11</sub> and isolation as S<sub>31</sub>.

Table 1 is a circuit operation of Design A during a transmission of Gaussian pulse from Port 1 to Port 2. In this operation, the series PIN diode of D1b was turned ON and series PIN diode of D1a was turned OFF with the voltage supplies of VCC 1 = +5 V and VCC 2 = -5 V. Meanwhile, shunt PIN diodes of D2b were in OFF state to ensure there

is no RF leakage from Port 1 to Port 2 and shunt PIN diodes of D2a was in ON state to isolate any RF signal from entering to Port 3.



Fig. 4. Series-shunt SPDT topology (Design A)

Table 1: Circuit operation of series-shunt SPDT switch

Label	Value/Condition
VCC 1	+ 5 V
VCC 2	- 5 V
D1a (series)	OFF state (reverse bias)
D2a (shunt)	ON state (forward bias)
D1b (series)	ON state (forward bias)
D2b (shunt)	OFF state (reverse bias)

In the mathematical analysis of the insertion loss ( $S_{21}$ ) and return loss ( $S_{11}$ ), the ABCD matrix of series-shunt SPDT for transmission of the Gaussian pulse from Port 1 to Port 2 is given by,

(1) 
$$\begin{bmatrix} T_{12} \end{bmatrix} = \begin{bmatrix} T_{D1b} \end{bmatrix} \begin{bmatrix} T_{D2b} \end{bmatrix}$$
$$= \begin{bmatrix} 1 & R_f + j\omega L_s \\ 0 & 1 \end{bmatrix} \begin{bmatrix} 1 & 0 \\ \frac{1}{R_r + j\omega L_s + \frac{1}{j\omega C_j}} & 1 \end{bmatrix}$$
$$= \begin{bmatrix} \left( \frac{R_f + j\omega L_s}{R_r + j\omega L_s + \frac{1}{j\omega C_j}} + 1 \right) & \left( R_f + j\omega L_s \right) \\ \left( \frac{1}{R_r + j\omega L_s + \frac{1}{j\omega C_j}} \right) & 1 \end{bmatrix}$$

where  $R_f$  is forward bias resistance,  $L_s$  is series inductance,  $R_r$  is reverse bias resistance and  $C_j$  is junction capacitance in the PIN diode [3].

For the insertion loss, the ABCD matrix of (1) is converted to S-parameter of  $S_{21}$ . Thus,

$$S_{21} = \frac{2}{A + \frac{B}{Z_0} + CZ_0 + D}$$

(2)

$$=\frac{2}{2+\frac{R_f+j\omega L_s}{Z_0}+\frac{Z_0}{R_r+j\omega L_s+\frac{1}{j\omega C_j}}+\frac{R_f+j\omega L_s}{R_r+j\omega L_s+\frac{1}{j\omega C_j}}}$$

By referring to (2), very low insertion loss can be obtained if the PIN diode impedance during ON state is close to zero and the PIN diode impedance during OFF state is very high. In this case, the characteristic impedance  $Z_0$  is normalized to 1.

For the return loss, the ABCD matrix of (1) is converted to S-parameter of  $S_{11}$ . Thus,

$$S_{11} = \frac{A + \frac{B}{Z_0} - CZ_0 - D}{A + \frac{B}{Z_0} + CZ_0 + D}$$

(3)

$$=\frac{\left(\frac{R_f+j\omega L_s}{R_r+j\omega L_s+\frac{1}{j\omega C_j}}+\frac{R_f+j\omega L_s}{Z_0}-\frac{Z_0}{R_r+j\omega L_s+\frac{1}{j\omega C_j}}\right)}{\left(2+\frac{R_f+j\omega L_s}{Z_0}+\frac{Z_0}{R_r+j\omega L_s+\frac{1}{j\omega C_j}}+\frac{R_f+j\omega L_s}{R_r+j\omega L_s+\frac{1}{j\omega C_j}}\right)}$$

It can be seen in (3) where a very high return loss can be obtained if the PIN diode impedance during ON state is close to zero and the PIN diode impedance during OFF state is very high. In this case, the characteristic impedance  $Z_0$  is normalized to 1.

In the mathematical analysis for the isolation ( $S_{31}$ ), the ABCD matrix of series-shunt SPDT for isolation of the Gaussian pulse from Port 1 to Port 3 is given by,

$$\begin{bmatrix} T_{IJ} \end{bmatrix} = \begin{bmatrix} T_{DIa} \end{bmatrix} \begin{bmatrix} T_{D2a} \end{bmatrix}$$

$$= \begin{bmatrix} 1 \quad R_r + j\omega L_s + \frac{1}{j\omega C_j} \\ 0 \quad 1 \end{bmatrix} \begin{bmatrix} 1 \quad 0 \\ \frac{1}{R_f + j\omega L_s} \quad 1 \end{bmatrix}$$

$$= \begin{bmatrix} \left( 1 + \frac{R_r + j\omega L_s + \frac{1}{j\omega C_j}}{R_f + j\omega L_s} \right) \quad \left( R_r + j\omega L_s + \frac{1}{j\omega C_j} \right) \\ \left( \frac{1}{R_f + j\omega L_s} \right) \quad 1 \end{bmatrix}$$

The ABCD matrix of (4) is then converted to S-parameter of  $S_{31}$ . Thus,



From (5), it shows that a very high isolation can be obtained if the PIN diode impedance during ON state is close to zero and the PIN diode impedance during OFF state is very high. In this case, the characteristic impedance  $Z_0$  is normalized to 1.

Therefore, it can be concluded that the two conditions of ON and OFF states of the PIN diodes could influence the performance of insertion loss, return loss and isolation. As reported in [24], additional shunt PIN diodes can be added in order to increase isolation. Therefore, Fig. 5 is the other three topologies (Design B, C and D) for the analysis of UWB isolation performance and the trade off with insertion loss and return loss. The analyses were done in Advanced Design System (ADS) software. All the SPDT switch topologies are summarized in Table 2 where Design A is a series-shunt PIN diode, Design B is a series-shunt-shunt PIN diode, Design C is a series-shunt-shunt-shunt PIN diodes and Design D is a series-shunt-shunt-shunt-shunt PIN diode.



Fig. 5. Other SPDT switch topologies for Design B, C and D

Table 2.	Summary	of SDDT	switch	topologio	-
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Design	Switch Topology	PIN diodes
Design A	series-shunt	D1a, D2a,
Design A		D1b, D2b
Dooign P	corico chunt chunt	D1a – D3a,
Design	series-snunt-snunt	D1b – D3b
Design C	corico chunt chunt chunt	D1a – D4a,
Design C	series-snum-snum-snum	D1b – D4b
Design D	corico obunt obunt obunt obunt	D1a – D5a,
Design D	series-snum-snum-snum-snum	D1b – D5b

# Performance Analysis of Different Topologies of SPDT Switch

Fig. 6 shows a comparison of simulation results between four SPDT switch topologies. MACOM's PIN diodes (MA4SPS402) model were used in these simulations. Fig. 6(a), (b) and (c) show the performance results of return loss ( $S_{11}$ ), insertion loss ( $S_{21}$ ) and isolation ( $S_{31}$ ) respectively. All designs simulated from 1 to 11 GHz so that it should be enough to see its capability in the UWB frequency (with 10 GHz bandwidth) for microwave medical imaging.

Overall, Design A, B, C and D produces very good  $S_{11}$  across the frequency range of 1.6 GHz to 11 GHz, as can be seen in Fig. 6(a). It was found that by adding more PIN diodes, the return loss was almost in the same response with the targeted return loss, higher than 10 dB, across the frequency range. For all designs, the lowest  $S_{11}$  was appear at 1 GHz of frequency. By contrast, Design A achieved the highest  $S_{11}$  at 10.2 GHz.

Fig. 6(b) shows that the  $S_{21}$  of Design A, B, C and D remained steady between 2 and 7 GHz at approximately 0 dB. then, it goes up at 7.5 GHz before it turns back closing to 0 dB. As can be seen, Design D showed the highest  $S_{21}$  at 7.5 GHz which still less than 1 dB. It was found that by adding more shunt PIN diodes, the  $S_{21}$  was almost in the same response with the desired result, less than 5 dB, across the simulated frequency range.

Fig. 6(c) shows that the isolations of Design A, B, C and D decreased at frequencies between 1 and 7.5 GHz before they remain steady at higher frequencies. Design D produced the highest value of isolation, about 88 dB, at 1 GHz. On the other hand, Design C produced the lowest value of isolation, about 16 dB, at 7.5 GHz. It was found that by adding more shunt PIN diodes, the isolation was improved significantly. It can be seen in average of additional 20 dB isolation improvement between Design A and D across the frequencies. Thus, for the isolation improvement, as a conclusion, there is a fact that the combination of series and shunt PIN diodes is the most chosen topology by designers.





For overall comparison, the simulation results of these four topologies showed very good outcomes in terms of return loss, insertion loss and isolation. Meanwhile, compared to other designs, Design D created very high isolation. However, Design B, also, showed a noticeable high isolation with less number of PIN diodes than Design D. Therefore, by considering the overall performances based on the minimum number of shunt PIN diodes, Design B was chosen for the cascaded SPDT in SP8T switch design due to its acceptable isolation with good insertion loss and return loss across the simulated frequency range.

Table 3 summarizes the simulation result of Design A, B, C and D. It lists the minimum (min.) and maximum (max.) results of return loss ( $S_{11}$ ), insertion loss ( $S_{21}$ ) and isolation ( $S_{31}$ ) across the simulated frequency range (1 to 11 GHz).

Table 3. Summary of simulation result for SPDT switch topologies of Design A, B, C and D

Design	S <sub>11</sub> (dB) Min & Max	S <sub>21</sub> (dB) Min & Max	S <sub>31</sub> (dB) Min & Max
Design A	6 & 51	0.1 & 2	20 & 61
Design B	6 & 36	0.1 & 2	18 & 70
Design C	6 & 48	0.1 & 2	16 & 79
Design D	6 & 39	0.1 & 2	17 & 88

### Simulation and Measurement Results of SP8T Switch

Fig. 7 shows the fabricated SP8T switch with cascaded SPDT switches according to diagram in Fig. 3. As results, Fig. 8 shows the comparison between simulated and measured results of the SP8T switch for return loss (S<sub>11</sub>), insertion loss (S<sub>21</sub>) and isolation (S<sub>31</sub>). These simulations and measurements were performed in the condition of the transmission of the Gaussian pulse from Port 1 to Port 2 and the isolation was at unused Antenna at Port 3. The isolation performance analysis was carried out in order to see the minimum isolation of the SP8T switch for microwave imaging system due to the single SPDT switch in the SP8T switch circuit.



Fig. 7. Fabricated SP8T switch with cascaded SPDT switches

As shown in Fig. 8(a), the simulated return loss (S<sub>11</sub>) showed almost a very good result from 1.7 GHz and above. The highest simulated S<sub>11</sub> was around 44 dB at 3.9 GHz. Although the measured S<sub>11</sub> was different compared to simulated result, but it was higher than 10 dB from 3 to 10 GHz.

As can be seen from Fig. 8(b), the simulated insertion loss ( $S_{21}$ ) was neared to 0 dB from 1 to 8 GHz. Then, there was a slight increase in the simulated  $S_{21}$  in the frequencies between 8 and 9 GHz before it had been decreased in the frequencies from 9 to 10 GHz. However, the measured  $S_{21}$  was lower than 5 dB from 1 GHz to 7.5 GHz, thus the useable insertion loss bandwidth was 6.5 GHz.

Fig. 8(c) shows the comparison between simulation and measurement results of isolation ( $S_{31}$ ). Overall, the simulated and measured isolation results were very high across the operation frequency. Besides, the  $S_{31}$  in the measurement was less than in the simulation at lower frequencies (1 to 7 GHz), and vice versa at higher frequencies (8 to 11 GHz). The simulated isolation produced more than 70 dB at 1 GHz before it decreased to 20 dB at 4 GHz. Then, there was a slight increase of the  $S_{31}$  between 4 and 6 GHz, and the isolation performance remained steady from 6 to 11 GHz.

Table 4 lists the summary of simulation and measurement results of SP8T switch. This table lists the minimum (min.) and maximum (max.) results of return loss  $(S_{11})$ , insertion loss  $(S_{21})$  and isolation  $(S_{31})$  across the frequency range from 1 until 11 GHz. a)



Fig. 8. Simulation versus measurement of SP8T switch; (a) return loss (S\_{11}), (b) insertion loss (S\_{21}) and (c) isolation (S\_{31})

Table 4. Summary of simulation and measurement results of SP8T switch

	S <sub>11</sub> (dB) Min & Max	S <sub>21</sub> (dB) Min & Max	S <sub>31</sub> (dB) Min & Max
Simulation	9 & 44	0 & 4	20 & 72
Measurement	3 & 30	2 & 22	20 & 60

Table 5 is a table for the performances of SP8T switch by referring to design specification. Generally, the proposed SP8T switch using cascaded SPDT switches produced a wide bandwidth of return loss, insertion loss and isolation. However, the usable isolation bandwidth was 9.3 GHz (sim.) and 4.5 GHz (meas.) due to low return loss at lower frequencies (1.7 GHz in simulation, 3 GHz in measurement) and higher insertion loss performance at higher frequencies (7.5 GHz in measurement). Meaning that, although the isolation bandwidth is 10 GHz (sim. and meas.), however, it was limited by the performance of return loss and insertion loss. In addition, low insertion loss in the measurement can be achieved at higher frequencies if a proper soldering of the components is carried out, thus increasing the usable isolation bandwidth in the measurement result.

Table 5. Performances of SP8T switch by referring to design specification

Performance Parameter	Specification	Bandwidth
Return Loss (S <sub>11</sub> )	> 10 dB	<u>1.7</u> – 11 GHz (sim) BW = 9.3 GHz  <u>3</u> – 10.5 GHz (meas) BW = 7.5 GHz
Insertion Loss (S <sub>21</sub> )	< 5 dB	1 – 11 GHz (sim) BW = 10 GHz  1 – <u>7.5</u> GHz (meas) BW = 6.5 GHz
Isolation (S <sub>31</sub> )	> 20 dB	1 – 11 GHz (sim) BW = 10 GHz  1 – 11 GHz (meas) BW = 10 GHz

### Conclusion

A SP8T switch using cascaded SPDT switches was designed in the frequency range from 1 to 11 GHz. The proposed SP8T switch is to control eight antenna array in the monostatic microwave imaging system for medical application. A mathematical modeling was done for a simple series-shunt SPDT switch for the analysis of insertion loss, return loss and isolation. It was found that, theoretically two conditions of ON and OFF states of the PIN diodes could influence the performance of insertion loss, return loss and isolation. Then, a proper selection of SPDT switch topologies was carried out in a simulation software in order to get ultra wideband (UWB) performance of the SP8T switch design. In the simulation analysis, by considering the overall performances based on the minimum number of shunt PIN diodes, Design B (series-shunt-shunt) was chosen for the cascaded SPDT in SP8T switch design due to its acceptable isolation with good insertion loss and return loss across the simulated frequency range. Finally, in the simulation analysis of the SP8T switch, it produced 9.3 GHz of isolation bandwidth that can be used for a Gaussian pulse up to 9.3 GHz bandwidth (from 1.7 GHz to 11 GHz frequency spectrum).

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